CPU-Z TXT Report

Binaries

CPU-Z version 1.76.0.x64

Processors

Number of processors 1 Number of threads 4

APICs

Processor 0

1. - Core 0
2. - Thread 0 0
3. - Core 1
4. - Thread 0 1
5. - Core 2
6. - Thread 0 2
7. - Core 3
8. - Thread 0 3

Timers

```
ACPI timer       3.580 MHz
HPET timer       25.000 MHz
Perf timer       2.943 MHz
Sys timer        1.000 KHz
```

Processors Information

Processor 1 ID = 0

```
Number of cores     4 (max 4)
Number of threads   4 (max 4)
Name            AMD Phenom II X4 945
Codename        Deneb
```

```
Specification       AMD Phenom(tm) II X4 945 Processor
Package       Socket AM3 (938)
CPUID       F.4.3
Extended CPUID       10.4
Brand ID       13
Core Stepping       RB-C3
Technology       45 nm
TDP Limit       95.9 Watts
Core Speed       803.5 MHz
Multiplier x Bus Speed   4.0 x 200.9 MHz
HT Link speed       1004.4 MHz
Stock frequency       3000 MHz
Instructions sets   MMX (+), 3DNow! (+), SSE, SSE2, SSE3, SSE4A, x86-64,
AMD-V
L1 Data cache       4 x 64 KBytes, 2-way set associative, 64-byte line size
L1 Instruction cache    4 x 64 KBytes, 2-way set associative, 64-byte line
size
L2 cache       4 x 512 KBytes, 16-way set associative, 64-byte line size
L3 cache       6 MBytes, 48-way set associative, 64-byte line size
FID/VID Control       yes
FID range       4.0x - 15.0x
Max VID       1.275 V
# of P-States       4
P-State       FID 0xE - VID 0x16 - IDD 19 (15.00x - 1.275 V)
P-State       FID 0x7 - VID 0x1E - IDD 15 (11.50x - 1.175 V)
P-State       FID 0x2 - VID 0x26 - IDD 13 (9.00x - 1.075 V)
P-State       FID 0x100 - VID 0x32 - IDD 7 (4.00x - 0.925 V)
```

```
Package Type       0x1
Model       45
String 1       0x3
String 2       0x6
Page       0x0
CmpCap       4
ApicIdCoreSize       4
TDC Limit       76 Amps
Boosted P-States   0
Max non-turbo ratio 15.00x
Max turbo ratio       15.00x
Max CPU COF       30
Core Performance Boost  no
P-State       0, FID 0xE - VID 0x16 (15.00x - 1.275 V)
P-State       1, FID 0x7 - VID 0x1E (11.50x - 1.175 V)
P-State       2, FID 0x2 - VID 0x26 (9.00x - 1.075 V)
P-State       3, FID 0x100 - VID 0x32 (4.00x - 0.925 V)
Attached device    PCI device at bus 0, device 24, function 0
Attached device    PCI device at bus 0, device 24, function 1
Attached device    PCI device at bus 0, device 24, function 2
Attached device    PCI device at bus 0, device 24, function 3
Attached device    PCI device at bus 0, device 24, function 4
TSC       3013.7 MHz
```

```
PERFEVT           3012.3 MHz


Temperature 0        0°C (32°F) [0x0] (Core #0)
Power 0           25.90 W (Package)
```

Thread dumps

CPU Thread 0

```
APIC ID           0
Topology          Processor ID 0, Core ID 0, Thread ID 0
Type              02004009h
Max CPUID level    00000005h
Max CPUID ext. level    8000001Bh
Cache descriptor    Level 1, I, 64 KB, 1 thread(s)
Cache descriptor    Level 1, D, 64 KB, 1 thread(s)
Cache descriptor    Level 2, U, 512 KB, 1 thread(s)
Cache descriptor    Level 3, U, 6 MB, 4 thread(s)
```

```
CPUID
0x00000000    0x00000005    0x68747541    0x444D4163    0x69746E65
0x00000001    0x00100F43    0x00040800    0x00802009    0x178BFBFF
0x00000002    0x00000000    0x00000000    0x00000000    0x00000000
0x00000003    0x00000000    0x00000000    0x00000000    0x00000000
0x00000004    0x00000000    0x00000000    0x00000000    0x00000000
0x00000005    0x00000040    0x00000040    0x00000003    0x00000000
0x80000000    0x8000001B    0x68747541    0x444D4163    0x69746E65
0x80000001    0x00100F43    0x10001AD6    0x000037FF    0xEFD3FBFF
0x80000002    0x20444D41    0x6E656850    0x74286D6F    0x4920296D
0x80000003    0x34582049    0x35343920    0x6F725020    0x73736563
0x80000004    0x0000726F    0x00000000    0x00000000    0x00000000
0x80000005    0xFF30FF10    0xFF30FF20    0x40020140    0x40020140
0x80000006    0x20800000    0x42004200    0x02008140    0x0030B140
0x80000007    0x00000000    0x00000000    0x00000000    0x000001F9
0x80000008    0x00003030    0x00000000    0x00002003    0x00000000
0x80000009    0x00000000    0x00000000    0x00000000    0x00000000
0x8000000A    0x00000001    0x00000040    0x00000000    0x0000000F
0x8000000B    0x00000000    0x00000000    0x00000000    0x00000000
0x8000000C    0x00000000    0x00000000    0x00000000    0x00000000
0x8000000D    0x00000000    0x00000000    0x00000000    0x00000000
0x8000000E    0x00000000    0x00000000    0x00000000    0x00000000
0x8000000F    0x00000000    0x00000000    0x00000000    0x00000000
0x80000010    0x00000000    0x00000000    0x00000000    0x00000000
0x80000011    0x00000000    0x00000000    0x00000000    0x00000000
0x80000012    0x00000000    0x00000000    0x00000000    0x00000000
0x80000013    0x00000000    0x00000000    0x00000000    0x00000000
0x80000014    0x00000000    0x00000000    0x00000000    0x00000000
0x80000015    0x00000000    0x00000000    0x00000000    0x00000000
0x80000016    0x00000000    0x00000000    0x00000000    0x00000000
```

```
0x80000017      0x00000000      0x00000000      0x00000000      0x00000000
0x80000018      0x00000000      0x00000000      0x00000000      0x00000000
0x80000019      0xF0300000      0x60100000      0x00000000      0x00000000
0x8000001A      0x00000003      0x00000000      0x00000000      0x00000000
0x8000001B      0x0000001F      0x00000000      0x00000000      0x00000000


MSR 0x0000001B      0x00000000      0xFEE00900
MSR 0xC0010114      0x00000000      0x00000018
MSR 0xC0010061      0x00000000      0x00000030
MSR 0xC0010062      0x00000000      0x00000000
MSR 0xC0010063      0x00000000      0x00000003
MSR 0xC0010064      0x800001BB      0x3C002C0E
MSR 0xC0010065      0x80000194      0x3C003C07
MSR 0xC0010066      0x80000185      0x3C004C02
MSR 0xC0010067      0x80000147      0x3C006440
MSR 0xC0010068      0x00000000      0x00000000
MSR 0xC0010058      0x00000000      0xE0000021
MSR 0xC0010071      0x30BC00B3      0x3C002C0E
MSR 0xC0010015      0x00000000      0x01000010
MSR 0xC001001F      0x40584000      0x00000008
MSR 0xC0010071      0x30BC00B3      0x3C002C0E
MSR 0xC0010070      0x00000000      0x3C002C0E
```

Chipset

Northbridge NVIDIA GeForce 7025 rev. A3 Southbridge NVIDIA nForce 630a rev. A2 Graphic Interface PCI-Express PCI-E Link Width x16 PCI-E Max Link Width x16 Memory Type DDR3 Memory Size 8 GBytes Channels Dual, (Unganged) Memory Frequency 669.6 MHz (3:10) CAS# latency (CL) 9.0 RAS# to CAS# delay (tRCD) 9 RAS# Precharge (tRP) 9 Cycle Time (tRAS) 24 Bank Cycle Time (tRC) 33 Command Rate (CR) 2T Uncore Frequency 2008.8 MHz

Memory SPD

DIMM # 1

```
SMBus address        0x50
Memory type      DDR3
Module format        UDIMM
Manufacturer (ID)   Team Group Inc. (7F7F7F7FEF0000000000)
Size            2048 MBytes
Max bandwidth       PC3-10700 (667 MHz)
Part number     Team--Elite-1333
Manufacturing date  Week 23/Year 10
Number of banks      8
Nominal Voltage      1.50 Volts
EPP         no
XMP         no
```

AMP	no

JEDEC timings table CL-tRCD-tRP-tRAS-tRC @ frequency

```
JEDEC #1          6.0-6-6-17-23 @ 457 MHz
JEDEC #2          7.0-7-7-20-27 @ 533 MHz
JEDEC #3          8.0-8-8-22-30 @ 609 MHz
JEDEC #4          9.0-9-9-24-33 @ 666 MHz
```

DIMM # 2

```
SMBus address        0x51
Memory type      DDR3
Module format        UDIMM
Manufacturer (ID)    Team Group Inc. (7F7F7F7FEF0000000000)
Size             2048 MBytes
Max bandwidth        PC3-10700 (667 MHz)
Part number      Team--Elite-1333
Manufacturing date   Week 24/Year 10
Number of banks      8
Nominal Voltage      1.50 Volts
EPP          no
XMP          no
AMP          no
```

JEDEC timings table CL-tRCD-tRP-tRAS-tRC @ frequency

```
JEDEC #1          6.0-6-6-17-23 @ 457 MHz
JEDEC #2          7.0-7-7-20-27 @ 533 MHz
JEDEC #3          8.0-8-8-22-30 @ 609 MHz
JEDEC #4          9.0-9-9-24-33 @ 666 MHz
```

DIMM # 3

```
SMBus address        0x52
Memory type      DDR3
Module format        UDIMM
Manufacturer (ID)    Kingston (7F98000000000000000)
Size             4096 MBytes
Max bandwidth        PC3-10700 (667 MHz)
Part number      99U5471-020.A00LF
Serial number        1B391D3C
Manufacturing date   Week 30/Year 12
Number of banks      8
Nominal Voltage      1.50 Volts
EPP          no
XMP          no
AMP          no
```

JEDEC timings table CL-tRCD-tRP-tRAS-tRC @ frequency

```
JEDEC #1          6.0-6-6-17-23 @ 457 MHz
JEDEC #2          7.0-7-7-20-27 @ 533 MHz
JEDEC #3          8.0-8-8-22-30 @ 609 MHz
JEDEC #4          9.0-9-9-24-33 @ 666 MHz
```

DIMM # 1 SPD registers

```
     00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00   92 10 0B 02 03 19 00 01 03 52 01 08 0C 00 3C 00
10   69 78 69 30 69 11 20 89 00 05 3C 3C 00 F0 83 05
20   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
30   00 00 00 00 00 00 00 00 00 00 00 00 0F 11 01 01
40   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
60   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70   00 00 00 00 00 04 EF 00 0A 1D 00 00 00 00 F1 DC
80   54 65 61 6D 2D 2D 45 6C 69 74 65 2D 31 33 33 33
90   00 00 00 00 04 EF 00 00 00 00 00 00 00 00 00 00
A0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0   FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
C0   FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
D0   FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
E0   10 10 07 12 02 1F FF FF FF FF FF FF FF FF FF FF
F0   FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
```

DIMM # 2 SPD registers

```
     00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00   92 10 0B 02 03 19 00 01 03 52 01 08 0C 00 3C 00
10   69 78 69 30 69 11 20 89 00 05 3C 3C 00 F0 83 05
20   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
30   00 00 00 00 00 00 00 00 00 00 00 00 0F 11 01 01
40   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
60   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70   00 00 00 00 00 04 EF 00 0A 1E 00 00 00 00 F1 DC
80   54 65 61 6D 2D 2D 45 6C 69 74 65 2D 31 33 33 33
90   00 00 00 00 04 EF 00 00 00 00 00 00 00 00 00 00
A0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0   FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
C0   FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
D0   FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
E0   10 10 07 20 02 5F FF FF FF FF FF FF FF FF FF FF
F0   FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
```

DIMM # 3 SPD registers

```
     00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00   92 10 0B 02 03 19 00 09 03 52 01 08 0C 00 3C 00
10   69 78 69 30 69 11 20 89 00 05 3C 3C 00 F0 83 81
```

```
20   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
30   00 00 00 00 00 00 00 00 00 00 00 00 04 11 07 01
40   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
60   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70   00 00 00 00 00 01 98 05 12 30 1B 39 1D 3C 28 A0
80   39 39 55 35 34 37 31 2D 30 32 30 2E 41 30 30 4C
90   46 20 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 5A
```

Monitoring

Mainboard Model M4N68T Pro (0x000002E2 - 0x15F6CDB8)

LPCIO

LPCIO Vendor ITE LPCIO Model IT8721 LPCIO Vendor ID 0x90 LPCIO Chip ID 0x8721 LPCIO Revision ID 0x3 Config Mode I/O address 0x2E Config Mode LDN 0x4 Config Mode registers

```
     00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00   00 00 00 00 00 00 00 04 00 00 00 00 00 00 00 00
10   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
20   87 21 03 11 00 00 FF 00 00 00 00 00 07 00 00 00
30   01 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
40   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
60   02 90 02 30 00 00 00 00 00 00 00 00 00 00 00 00
70   00 02 00 00 04 04 00 00 00 00 00 00 00 00 00 00
```

Register space LPC, base address = 0x0290

Hardware Monitors

Hardware monitor ITE IT8721

```
Voltage 0        12.16 Volts [0xEC] (+12V)
Voltage 1        5.12 Volts [0xEF] (+5V)
Voltage 2        0.92 Volts [0x4D] (CPU VCORE)
Voltage 3        1.67 Volts [0x8B] (VIN3)
Voltage 4        0.91 Volts [0x4C] (VIN4)
```

```
Voltage 5        3.34 Volts [0xDF] (+3.3V)
Voltage 6        1.60 Volts [0x85] (VIN6)
Voltage 7        2.38 Volts [0xC6] (VIN7)
Voltage 8        1.67 Volts [0x8B] (VIN8)
Temperature 0       46°C (114°F) [0x2E] (CPU)
Temperature 1       29°C (84°F) [0x1D] (Mainboard)
Fan 0            2033 RPM [0x14C] (CPU)
Fan PWM 0        99 pc [0x7F] (FANPWM0)
Fan PWM 1        99 pc [0x7F] (FANPWM1)
Fan PWM 2        99 pc [0x7F] (FANPWM2)
```

Register space LPC, base address = 0x0290

```
     00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00   19 02 FB 03 00 00 00 00 00 80 58 0F 00 4D FF 00
10   21 A8 45 37 87 7F 7F 7F 01 FF 00 B1 57 58 58 58
20   EB EF 5A 8B 4C DF 85 C6 8B 2E 1D 80 80 0F 42 42
30   49 4B 3E EF D0 06 72 4C 46 75 9F CD C9 E1 E4 E0
40   19 6C 87 9F D1 68 5F 40 AD 6A D4 00 FF FF FF FF
50   27 18 7F 7F 7F 40 00 00 90 00 38 12 60 00 00 00
60   7F 7F 7F FF 00 00 00 FF 7F 7F 7F FF 00 00 00 FF
70   7F 7F 7F FF 00 00 00 FF FF FF FF FF FF FF FF FF
80   00 00 00 00 00 00 00 00 00 00 00 00 00 BA 02 E3
90   FF 00 00 00 FF 00 00 00 FF FF FF FF FF FF FF FF
A0   CB 4C 4C 4C 4C 4C 4C FF FF FF FF FF FF FF FF FF
B0   FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
C0   FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
D0   FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
E0   FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
F0   FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF
```

Hardware monitor NVIDIA NVAPI

```
Voltage 0        0.99 Volts [0x3DE] (VIN0)
Temperature 0       41°C (105°F) [0x29] (TMPIN0)
Fan PWM 0        52 pc [0x34] (FANPWMIN0)
Clock Speed 0       700.00 MHz [0x2BC] (Graphics)
Clock Speed 1       800.00 MHz [0x2BC] (Memory)
Clock Speed 2       1400.00 MHz [0x2BC] (Processor)
```

PCI Devices

Register space PCI Express, base address = 0x0E0000000

Description RAM Memory Controller Location bus 0 (0x00), device 0 (0x00), function 0 (0x00)
Common header

```
Vendor ID        0x10DE
Model ID         0x03E2
```

```
Revision ID      0xA1
PI          0x00
SubClass         0x00
BaseClass        0x05
Cache Line       0x00
Latency          0x00
Header           0x00
```

PCI header

```
Subvendor ID         0x1043
Subsystem ID         0x83A4
Int. Line        0x00
Int. Pin         0x00
```

PCI capability

```
Caps class       HyperTransport
Caps offset      0x44
Caps revision        1.03
Interface type       Slave/Primary
Link 0 width (in/out)   16 bits/16 bits
Link 0 frequency     1000 MHz
Link 1 width (in/out)   8 bits/8 bits
Link 1 frequency     200 MHz
```

PCI capability

```
Caps class       HyperTransport
Caps offset      0xDC
Interface type       MSI Mapping
```

PCI registers

```
     00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00   DE 10 E2 03 06 00 B0 00 A1 00 00 05 00 00 00 00
10   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
20   00 00 00 00 00 00 00 00 00 00 00 00 43 10 A4 83
30   00 00 00 00 44 00 00 00 00 00 00 00 00 00 00 00
40   43 10 A4 83 08 DC 20 02 20 01 11 11 D0 00 00 00
50   23 06 7F 00 03 00 00 00 00 00 03 00 00 00 00 00
60   00 00 00 00 00 00 00 00 00 00 00 00 06 36 00 00
70   44 44 44 00 D0 09 00 00 11 00 00 00 11 11 88 00
80   23 99 88 00 FA 00 64 0D 03 00 00 00 7F 00 00 00
90   70 00 00 80 00 00 00 00 00 00 00 00 00 00 00 00
A0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0   00 00 00 00 01 01 01 01 00 00 00 00 00 00 00 00
C0   00 00 00 00 00 00 00 80 00 00 00 00 00 00 00 00
D0   00 00 00 00 00 00 00 00 00 00 00 00 08 00 01 A8
E0   00 00 E0 FE 00 00 00 00 06 00 00 00 20 10 00 00
```

```
F0   F0 FF FF FF 00 00 00 00 00 00 00 00 00 00 00 00
```

Description PCI to ISA Bridge Location bus 0 (0x00), device 1 (0x01), function 0 (0x00) Common header

```
Vendor ID        0x10DE
Model ID         0x03E1
Revision ID      0xA2
PI          0x00
SubClass         0x01
BaseClass        0x06
Cache Line       0x00
Latency          0x00
Header           0x80
```

PCI header

```
Address 0 (port)     0x00000900
Subvendor ID         0x1043
Subsystem ID         0x83A4
Int. Line        0x00
Int. Pin         0x00
```

PCI registers

```
     00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00   DE 10 E1 03 0F 00 A0 00 A2 00 01 06 00 00 80 00
10   01 09 00 00 00 00 00 00 00 00 00 00 00 00 00 00
20   00 00 00 00 00 00 00 00 00 00 00 00 43 10 A4 83
30   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
40   43 10 A4 83 00 00 D0 FE FA 3E FF 00 FA 3E FF 00
50   FA 3E FF 00 00 5A 62 02 00 00 00 05 33 00 2C 02
60   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70   10 00 FF FF C5 80 00 00 00 00 45 19 00 00 06 C0
80   09 20 00 8D 01 D8 00 00 00 00 40 01 FF 00 00 00
90   FF 7F 00 00 00 00 00 00 21 65 08 74 B9 0C 00 D0
A0   01 00 10 81 00 00 00 00 00 00 00 00 00 00 00 00
B0   00 00 00 00 30 02 AF 02 00 00 00 00 00 00 00 00
C0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0   00 00 00 00 00 00 00 00 00 00 00 FE FD 03 00 A0
E0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0   00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00
```

Description SMBus Controller Location bus 0 (0x00), device 1 (0x01), function 1 (0x01) Common header

```
Vendor ID        0x10DE
Model ID         0x03EB
Revision ID      0xA2
PI          0x00
```

```
SubClass          0x05
BaseClass         0x0C
Cache Line        0x00
Latency           0x00
Header            0x80
```

PCI header

```
Address 0 (port)     0x00000E00
Address 4 (port)     0x00000600
Address 5 (port)     0x00000700
Subvendor ID         0x1043
Subsystem ID         0x83A4
Int. Line       0x0A
Int. Pin        0x01
```

PCI capability

```
Caps class      Power Management
Caps offset     0x44
Caps version        1.1
```

PCI registers

```
     00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00   DE 10 EB 03 01 00 B0 00 A2 00 05 0C 00 00 80 00
10   01 0E 00 00 00 00 00 00 00 00 00 00 00 00 00 00
20   01 06 00 00 01 07 00 00 00 00 00 00 43 10 A4 83
30   00 00 00 00 44 00 00 00 00 00 00 00 0A 01 00 00
40   43 10 A4 83 01 00 02 C0 00 00 00 00 00 00 00 00
50   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
60   01 05 00 00 01 08 00 00 01 0D 00 00 00 00 00 00
70   00 00 00 00 00 00 C8 FE 00 00 00 00 01 11 00 00
80   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0   D4 30 80 00 01 00 00 00 00 00 00 00 00 00 00 00
D0   40 00 40 01 10 00 00 00 05 00 00 00 00 00 00 00
E0   80 10 04 00 04 40 02 07 80 22 00 20 41 44 44 11
F0   02 FF 1E BF 01 00 00 80 10 00 00 00 00 00 00 00
```

Description RAM Memory Controller Location bus 0 (0x00), device 1 (0x01), function 2 (0x02)
Common header

```
Vendor ID         0x10DE
Model ID          0x03F5
Revision ID       0xA2
PI          0x00
SubClass          0x00
```

```
BaseClass        0x05
Cache Line       0x00
Latency          0x00
Header           0x80
```

PCI header

```
Subvendor ID        0x1043
Subsystem ID        0x83A4
Int. Line        0x00
Int. Pin         0x00
```

PCI registers

```
     00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00   DE 10 F5 03 00 04 A0 00 A2 00 00 05 00 00 80 00
10   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
20   00 00 00 00 00 00 00 00 00 00 00 00 43 10 A4 83
30   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
40   43 10 A4 83 00 00 00 00 10 02 80 10 10 00 10 10
50   10 10 10 10 00 00 00 00 00 00 00 00 10 42 00 00
60   0B 00 00 00 C0 1C 52 06 21 00 10 0A 00 00 63 00
70   09 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80   00 00 00 00 00 00 40 00 00 00 00 00 A0 18 00 00
90   00 00 00 00 00 00 00 12 20 81 04 00 00 00 00 00
A0   00 14 00 06 00 00 00 00 00 00 00 01 00 00 00 00
B0   00 00 00 00 42 80 30 04 00 00 00 00 00 00 00 00
C0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

Description USB Controller (OHCI) Location bus 0 (0x00), device 2 (0x02), function 0 (0x00) Common header

```
Vendor ID        0x10DE
Model ID         0x03F1
Revision ID      0xA3
PI          0x10
SubClass         0x03
BaseClass        0x0C
Cache Line       0x00
Latency          0x00
Header           0x80
```

PCI header

```
Address 0 (memory)  0xDDFFB000
Subvendor ID        0x1043
Subsystem ID        0x83A4
```

```
Int. Line        0x17
Int. Pin         0x01
```

PCI capability

```
Caps class       Power Management
Caps offset      0x44
Caps version        1.1
```

PCI registers

```
     00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00   DE 10 F1 03 06 00 B0 00 A3 10 03 0C 00 00 80 00
10   00 B0 FF DD 00 00 00 00 00 00 00 00 00 00 00 00
20   00 00 00 00 00 00 00 00 00 00 00 00 43 10 A4 83
30   00 00 00 00 44 00 00 00 00 00 00 00 17 01 03 01
40   43 10 A4 83 01 00 02 FE 00 00 00 00 00 00 00 00
50   0D 00 00 00 1D 47 40 00 10 00 00 00 00 00 00 00
60   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

Description USB 2.0 Controller (EHCI) Location bus 0 (0x00), device 2 (0x02), function 1 (0x01)
Common header

```
Vendor ID        0x10DE
Model ID         0x03F2
Revision ID      0xA3
PI          0x20
SubClass         0x03
BaseClass        0x0C
Cache Line       0x00
Latency          0x00
Header           0x80
```

PCI header

```
Address 0 (memory)   0xDDFFAC00
Subvendor ID         0x1043
Subsystem ID         0x83A4
Int. Line        0x16
Int. Pin         0x02
```

PCI capability

```
Caps class       Debug Port
Caps offset      0x44
```

PCI capability

```
Caps class       Power Management
Caps offset      0x80
Caps version         1.1
```

PCI registers

```
     00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00   DE 10 F2 03 06 00 B0 00 A3 20 03 0C 00 00 80 00
10   00 AC FF DD 00 00 00 00 00 00 00 00 00 00 00 00
20   00 00 00 00 00 00 00 00 00 00 00 00 43 10 A4 83
30   00 00 00 00 44 00 00 00 00 00 00 00 16 02 03 01
40   43 10 A4 83 0A 80 98 20 00 00 00 00 00 00 00 00
50   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
60   20 20 01 00 00 60 18 85 C3 13 0F 01 00 00 00 00
70   00 00 08 00 00 10 20 80 89 3D B6 22 77 25 04 00
80   01 00 02 FE 00 80 00 00 00 00 00 00 15 16 00 00
90   00 01 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0   01 00 00 00 00 20 00 C0 00 00 00 00 00 00 00 00
B0   00 11 22 33 44 00 00 00 FF 03 00 00 00 00 00 00
C0   10 10 2D 0D 00 00 00 00 00 00 00 00 00 00 00 00
D0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0   00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00
```

Description PCI to PCI Bridge Location bus 0 (0x00), device 4 (0x04), function 0 (0x00) Common header

```
Vendor ID        0x10DE
Model ID         0x03F3
Revision ID      0xA1
PI          0x01
SubClass         0x04
BaseClass        0x06
Cache Line       0x00
Latency          0x00
Header           0x01
```

PCI header

```
Primary bus      0x00
Secondary bus       0x01
Int. Line        0x00
Int. Pin         0x00
```

PCI capability

```
Caps class        Subsystem Vendor
Caps offset       0xB8
SubVendor ID        0x1043
SubSystem ID        0x83A4
```

PCI capability

```
Caps class        HyperTransport
Caps offset       0x8C
Interface type      MSI Mapping
```

PCI registers

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00  DE 10 F3 03 07 04 B0 00 A1 01 04 06 00 00 01 00
10  00 00 00 00 00 00 00 00 00 01 01 40 F0 00 80 22
20  F0 FF 00 00 F0 FF 00 00 00 00 00 00 00 00 00 00
30  00 00 00 00 B8 00 00 00 00 00 00 00 00 00 02 02
40  00 00 73 07 01 00 02 00 07 00 00 00 00 00 48 00
50  00 00 00 00 00 00 00 00 FF 1F FF 1F 00 00 00 00
60  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80  00 06 00 00 00 00 00 00 00 00 00 00 08 00 01 A8
90  00 00 E0 FE 00 00 00 00 00 00 00 00 00 00 00 00
A0  04 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0  00 00 00 00 FF FF 00 00 0D 8C 00 00 43 10 A4 83
C0  43 10 A4 83 03 00 00 00 00 00 00 00 00 00 00 00
D0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

Description Multimedia device Location bus 0 (0x00), device 5 (0x05), function 0 (0x00) Common header

```
Vendor ID         0x10DE
Model ID          0x03F0
Revision ID       0xA2
PI          0x00
SubClass          0x03
BaseClass         0x04
Cache Line        0x00
Latency           0x00
Header            0x80
```

PCI header

```
Address 0 (memory)   0xDDFF4000
Subvendor ID         0x1043
```

```
Subsystem ID        0x8415
Int. Line        0x15
Int. Pin        0x02
```

PCI capability

```
Caps class        Power Management
Caps offset       0x44
Caps version        1.1
```

PCI capability

```
Caps class        Message Signalled Interrupts
Caps offset       0x50
```

PCI capability

```
Caps class        HyperTransport
Caps offset       0x6C
Interface type        MSI Mapping
```

PCI registers

```
     00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00   DE 10 F0 03 06 00 B0 00 A2 00 03 04 00 00 80 00
10   00 40 FF DD 00 00 00 00 00 00 00 00 00 00 00 00
20   00 00 00 00 00 00 00 00 00 00 00 00 43 10 15 84
30   00 00 00 00 44 00 00 00 00 00 00 00 15 02 02 05
40   43 10 15 84 01 50 02 C0 00 00 00 00 01 01 0F 00
50   05 6C 80 01 00 00 00 00 00 00 00 00 00 00 00 00
60   00 00 00 00 00 00 00 00 0F 00 00 00 08 00 03 A8
70   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0   00 00 00 00 00 00 00 47 00 29 00 00 00 00 00 00
```

Description IDE Controller Location bus 0 (0x00), device 6 (0x06), function 0 (0x00) Common header

```
Vendor ID        0x10DE
Model ID        0x03EC
Revision ID     0xA2
PI        0x8A
SubClass        0x01
BaseClass       0x01
Cache Line      0x00
```

```
Latency              0x00
Header               0x00
```

PCI header

```
Address 4 (port)     0x0000FFA0
Subvendor ID         0x1043
Subsystem ID         0x83A4
Int. Line      0x00
Int. Pin       0x00
```

PCI capability

```
Caps class        Power Management
Caps offset       0x44
Caps version         1.1
```

PCI registers

```
     00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00   DE 10 EC 03 05 00 B0 00 A2 8A 01 01 00 00 00 00
10   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
20   A1 FF 00 00 00 00 00 00 00 00 00 00 43 10 A4 83
30   00 00 00 00 44 00 00 00 00 00 00 00 00 00 03 01
40   43 10 A4 83 01 00 02 00 00 00 00 00 00 00 00 00
50   02 F0 04 00 00 00 00 00 A8 A8 A8 20 7F 00 FF 20
60   00 00 00 C0 00 00 00 00 00 00 00 00 00 00 00 00
70   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90   00 00 00 00 00 00 00 00 00 00 00 00 00 00 01 01
A0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0   00 00 00 00 00 00 00 00 10 00 00 00 00 00 00 00
```

Description Bridge device Location bus 0 (0x00), device 7 (0x07), function 0 (0x00) Common header

```
Vendor ID          0x10DE
Model ID           0x03EF
Revision ID      0xA2
PI          0x00
SubClass           0x80
BaseClass          0x06
Cache Line         0x00
Latency            0x00
Header             0x00
```

PCI header

```
Address 0 (memory)  0xDDFF9000
Subvendor ID        0x1043
Subsystem ID        0x83A4
Int. Line       0x14
Int. Pin        0x01
```

PCI capability

```
Caps class      Power Management
Caps offset     0x44
Caps version        1.1
```

PCI capability

```
Caps class      Message Signalled Interrupts
Caps offset     0x50
```

PCI capability

```
Caps class      HyperTransport
Caps offset     0x6C
Interface type      MSI Mapping
```

PCI registers

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00  DE 10 EF 03 06 00 B0 00 A2 00 80 06 00 00 00 00
10  00 90 FF DD 01 00 00 00 00 00 00 00 00 00 00 00
20  00 00 00 00 00 00 00 00 00 00 00 00 43 10 A4 83
30  00 00 00 00 44 00 00 00 00 00 00 00 14 01 01 14
40  43 10 A4 83 01 50 02 FE 00 01 00 00 08 00 00 20
50  05 6C 86 01 00 00 00 00 00 00 00 00 00 00 00 00
60  00 00 00 00 00 00 00 00 FF 00 00 00 08 00 03 A8
70  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0  00 00 00 00 10 00 00 00 42 01 00 00 00 00 00 00
```

Description IDE Controller Location bus 0 (0x00), device 8 (0x08), function 0 (0x00) Common header

```
Vendor ID       0x10DE
Model ID        0x03F6
Revision ID     0xA2
PI          0x85
SubClass        0x01
```

```
BaseClass        0x01
Cache Line       0x00
Latency          0x00
Header           0x80
```

PCI header

```
Address 0 (port)     0x0000D400
Address 1 (port)     0x0000D080
Address 2 (port)     0x0000D000
Address 3 (port)     0x0000CC00
Address 4 (port)     0x0000C880
Address 5 (memory)   0xDDFF8000
Subvendor ID         0x1043
Subsystem ID         0x83A4
Int. Line        0x14
Int. Pin         0x01
```

PCI capability

```
Caps class       Power Management
Caps offset      0x44
Caps version         1.1
```

PCI capability

```
Caps class       Message Signalled Interrupts
Caps offset      0xB0
```

PCI capability

```
Caps class       HyperTransport
Caps offset      0xCC
Interface type       MSI Mapping
```

PCI registers

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00  DE 10 F6 03 07 00 B0 00 A2 85 01 01 00 00 80 00
10  01 D4 00 00 81 D0 00 00 01 D0 00 00 01 CC 00 00
20  81 C8 00 00 00 80 FF DD 00 00 00 00 43 10 A4 83
30  00 00 00 00 44 00 00 00 00 00 00 00 14 01 03 01
40  43 10 A4 83 01 B0 02 00 00 00 00 00 00 00 00 00
50  2F 68 08 00 00 00 00 00 00 20 00 20 44 20 00 20
60  00 00 00 C6 41 0C 00 00 00 0F 06 42 00 00 00 00
70  2C 78 C4 40 01 10 00 00 01 10 00 00 20 00 20 01
80  00 00 00 C0 00 30 F6 B9 00 10 D4 B8 EE DF 73 FF
90  00 00 DD 3B 00 00 00 00 06 00 06 10 00 00 01 01
A0  08 00 00 0F 80 00 00 00 00 00 00 00 33 31 00 02
B0  05 CC 84 00 00 00 00 00 00 00 00 00 00 00 00 00
```

```
C0    00 00 00 00 00 00 00 00 0A 00 0A 00 08 00 03 A8
D0    08 00 00 44 00 08 00 00 08 00 00 44 00 08 00 00
E0    00 00 40 00 00 00 00 00 00 00 00 00 00 00 00 00
F0    00 00 00 00 00 00 00 00 00 00 0C 00 00 00 00 00
```

Description IDE Controller Location bus 0 (0x00), device 8 (0x08), function 1 (0x01) Common header

```
Vendor ID       0x10DE
Model ID        0x03F6
Revision ID     0xA2
PI          0x85
SubClass        0x01
BaseClass       0x01
Cache Line      0x00
Latency         0x00
Header          0x80
```

PCI header

```
Address 0 (port)    0x0000C800
Address 1 (port)    0x0000C480
Address 2 (port)    0x0000C400
Address 3 (port)    0x0000C080
Address 4 (port)    0x0000C000
Address 5 (memory)  0xDDFEF000
Subvendor ID        0x1043
Subsystem ID        0x83A4
Int. Line       0x15
Int. Pin        0x02
```

PCI capability

```
Caps class      Power Management
Caps offset     0x44
Caps version        1.1
```

PCI capability

```
Caps class      Message Signalled Interrupts
Caps offset     0xB0
```

PCI capability

```
Caps class      HyperTransport
Caps offset     0xCC
Interface type      MSI Mapping
```

PCI registers

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
```

```
00   DE 10 F6 03 07 00 B0 00 A2 85 01 01 00 00 80 00
10   01 C8 00 00 81 C4 00 00 01 C4 00 00 81 C0 00 00
20   01 C0 00 00 00 F0 FE DD 00 00 00 00 43 10 A4 83
30   00 00 00 00 44 00 00 00 00 00 00 00 15 02 03 01
40   43 10 A4 83 01 B0 02 00 00 00 00 00 00 00 00 00
50   2F 68 08 00 00 00 00 00 00 00 00 00 00 00 00 00
60   00 00 00 00 41 0C 00 00 00 0F 06 42 00 00 00 00
70   2C 78 C4 40 01 10 00 00 01 10 00 00 20 00 20 01
80   00 00 00 00 BE FD 5D 7E 00 00 B8 19 EE 5F FD 59
90   00 00 16 3E 00 00 00 00 06 00 06 10 00 00 01 01
A0   08 00 00 00 80 00 00 00 00 00 00 00 33 31 00 02
B0   05 CC 84 00 00 00 00 00 00 00 00 00 00 00 00 00
C0   00 00 00 00 00 00 00 00 0A 00 0A 00 08 00 03 A8
D0   08 00 00 44 00 08 08 00 08 00 00 44 00 08 08 00
E0   00 00 40 00 00 00 00 00 00 00 00 00 00 00 00 00
F0   00 00 00 00 00 00 00 00 00 00 0C 00 00 00 00 00
```

Description PCI to PCI Bridge Location bus 0 (0x00), device 9 (0x09), function 0 (0x00) Common header

```
Vendor ID        0x10DE
Model ID         0x03E8
Revision ID      0xA2
PI          0x00
SubClass         0x04
BaseClass        0x06
Cache Line       0x10
Latency          0x00
Header           0x01
```

PCI header

```
Primary bus      0x00
Secondary bus       0x02
Int. Line        0x00
Int. Pin         0x00
```

PCI capability

```
Caps class       Subsystem Vendor
Caps offset      0x40
SubVendor ID        0x10DE
SubSystem ID        0x0000
```

PCI capability

```
Caps class       Power Management
Caps offset      0x48
Caps version        1.1
```

PCI capability

```
Caps class        Message Signalled Interrupts
Caps offset       0x50
```

PCI capability

```
Caps class        HyperTransport
Caps offset       0x60
Interface type      MSI Mapping
```

PCI capability

```
Caps class        PCI Express
Caps offset       0x80
Device type       Root Port of PCI-E Root Complex
Port              0
Version           1.0
Physical slot       #0
Presence detect     yes
Link width        16x (max 16x)
```

Extended capabilities

```
Caps class        Virtual Channel
Caps offset       0x100
```

PCI registers

```
     00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00   DE 10 E8 03 07 04 10 00 A2 00 04 06 10 00 01 00
10   00 00 00 00 00 00 00 00 00 02 02 00 E1 E1 00 20
20   00 DE E0 DF 01 D0 F1 DB 00 00 00 00 00 00 00 00
30   00 00 00 00 40 00 00 00 00 00 00 00 00 00 1A 00
40   0D 48 00 00 DE 10 00 00 01 50 02 F8 00 00 00 00
50   05 60 93 00 0C F0 E0 FE 00 00 00 00 A2 49 00 00
60   08 80 01 A8 00 00 E0 FE 00 00 00 00 00 00 00 00
70   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80   10 00 41 01 01 80 00 00 10 28 00 00 01 3D 11 00
90   00 00 01 31 80 25 08 00 C0 01 48 01 00 00 00 00
A0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
100  02 00 01 00 00 00 00 00 00 00 00 00 00 02 00 00 00
110  00 00 00 00 FF 00 00 80 00 00 00 00 00 00 00 00
120  00 00 00 01 00 00 00 00 00 00 00 00 00 00 00 00
130  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

Description PCI to PCI Bridge Location bus 0 (0x00), device 11 (0x0B), function 0 (0x00) Common header

```
Vendor ID        0x10DE
Model ID         0x03E9
Revision ID      0xA2
PI          0x00
SubClass         0x04
BaseClass        0x06
Cache Line       0x10
Latency          0x00
Header           0x01
```

PCI header

```
Primary bus      0x00
Secondary bus       0x03
Int. Line        0x00
Int. Pin         0x00
```

PCI capability

```
Caps class       Subsystem Vendor
Caps offset      0x40
SubVendor ID        0x10DE
SubSystem ID        0x0000
```

PCI capability

```
Caps class       Power Management
Caps offset      0x48
Caps version        1.1
```

PCI capability

```
Caps class       Message Signalled Interrupts
Caps offset      0x50
```

PCI capability

```
Caps class       HyperTransport
Caps offset      0x60
Interface type      MSI Mapping
```

PCI capability

```
Caps class       PCI Express
Caps offset      0x80
Device type      Root Port of PCI-E Root Complex
Port             1
```

```
Version           1.0
Physical slot      #0
Presence detect    yes
Link width        1x (max 1x)
```

Extended capabilities

```
Caps class       Virtual Channel
Caps offset      0x100
```

PCI registers

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00  DE 10 E9 03 06 04 10 00 A2 00 04 06 10 00 01 00
10  00 00 00 00 00 00 00 00 00 03 03 00 F1 01 00 00
20  F0 DF F0 DF F1 FF 01 00 00 00 00 00 00 00 00 00
30  00 00 00 00 40 00 00 00 00 00 00 00 00 00 02 00
40  0D 48 00 00 DE 10 00 00 01 50 02 F8 00 00 00 00
50  05 60 93 00 0C F0 E0 FE 00 00 00 00 B2 49 00 00
60  08 80 01 A8 00 00 E0 FE 00 00 00 00 00 00 00 00
70  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80  10 00 41 01 01 80 00 00 10 28 00 00 11 3C 11 01
90  00 00 11 30 00 05 10 00 C0 01 48 01 00 00 00 00
A0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
100 02 00 01 00 00 00 00 00 00 00 00 00 02 00 00 00
110 00 00 00 00 FF 00 00 80 00 00 00 00 00 00 00 00
120 00 00 00 01 00 00 00 00 00 00 00 00 00 00 00 00
130 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

Description PCI to PCI Bridge Location bus 0 (0x00), device 12 (0x0C), function 0 (0x00) Common header

```
Vendor ID        0x10DE
Model ID         0x03E9
Revision ID      0xA2
PI          0x00
SubClass         0x04
BaseClass        0x06
Cache Line       0x10
Latency          0x00
Header           0x01
```

PCI header

```
Primary bus      0x00
```

```
Secondary bus       0x04
Int. Line       0x00
Int. Pin        0x00
```

PCI capability

```
Caps class      Subsystem Vendor
Caps offset     0x40
SubVendor ID        0x10DE
SubSystem ID        0x0000
```

PCI capability

```
Caps class      Power Management
Caps offset     0x48
Caps version        1.1
```

PCI capability

```
Caps class      Message Signalled Interrupts
Caps offset     0x50
```

PCI capability

```
Caps class      HyperTransport
Caps offset     0x60
Interface type      MSI Mapping
```

PCI capability

```
Caps class      PCI Express
Caps offset     0x80
Device type     Root Port of PCI-E Root Complex
Port            2
Version         1.0
Physical slot       #0
Presence detect     no
Link width      1x (max 1x)
```

Extended capabilities

```
Caps class      Virtual Channel
Caps offset     0x100
```

PCI registers

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00  DE 10 E9 03 04 04 10 00 A2 00 04 06 10 00 01 00
10  00 00 00 00 00 00 00 00 00 04 04 00 F1 01 00 00
20  F0 FF 00 00 F1 FF 01 00 00 00 00 00 00 00 00 00
```

```
30   00 00 00 00 40 00 00 00 00 00 00 00 00 00 02 00
40   0D 48 00 00 DE 10 00 00 01 50 02 F8 00 00 00 00
50   05 60 93 00 0C F0 E0 FE 00 00 00 00 92 49 00 00
60   08 80 01 A8 00 00 E0 FE 00 00 00 00 00 00 00 00
70   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80   10 00 41 01 01 80 00 00 10 28 00 00 11 3C 11 02
90   00 00 11 10 00 05 18 00 C0 01 00 00 00 00 00 00
A0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
100  02 00 01 00 00 00 00 00 00 00 00 00 02 00 00 00
110  00 00 00 00 FF 00 00 80 00 00 00 00 00 00 00 00
120  00 00 00 01 00 00 00 00 00 00 00 00 00 00 00 00
130  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

Description Host Bridge Location bus 0 (0x00), device 24 (0x18), function 0 (0x00) Common header

```
Vendor ID        0x1022
Model ID         0x1200
Revision ID      0x00
PI          0x00
SubClass         0x00
BaseClass        0x06
Cache Line       0x00
Latency          0x00
Header           0x80
```

PCI header

```
Subvendor ID         0x0000
Subsystem ID         0x0000
Int. Line        0x00
Int. Pin         0x00
```

PCI capability

```
Caps class       HyperTransport
Caps offset      0x80
Caps revision        3.00
Interface type       Host/Secondary
Device number        0
Link 0 width (in/out)   16 bits/16 bits
Link 0 frequency     1000 MHz
```

PCI registers

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
```

```
00    22 10 00 12 00 00 10 00 00 00 00 06 00 00 80 00
10    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
20    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
30    00 00 00 00 80 00 00 00 00 00 00 00 00 00 00 00
40    01 02 04 00 01 02 04 00 01 02 04 00 01 02 04 00
50    01 02 04 00 01 02 04 00 01 02 04 00 01 02 04 00
60    00 00 03 00 E0 00 00 00 20 A8 4E 00 30 F8 00 00
70    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80    08 00 01 21 20 20 11 11 60 06 F5 8F 13 00 00 00
90    92 02 85 04 00 00 00 00 07 00 00 00 00 00 00 00
A0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

Description Host Bridge Location bus 0 (0x00), device 24 (0x18), function 1 (0x01) Common header

```
Vendor ID          0x1022
Model ID           0x1201
Revision ID        0x00
PI          0x00
SubClass           0x00
BaseClass          0x06
Cache Line         0x00
Latency            0x00
Header             0x80
```

PCI header

```
Subvendor ID        0x0000
Subsystem ID        0x0000
Int. Line          0x00
Int. Pin           0x00
```

PCI registers

```
      00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00    22 10 01 12 00 00 00 00 00 00 00 06 00 00 80 00
10    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
20    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
30    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
40    03 00 00 00 00 00 2F 02 00 00 00 00 00 00 00 00
50    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
60    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80    03 00 E0 00 80 FF EF 00 00 00 00 00 00 00 00 00
90    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0    00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0    03 0A 00 00 00 0B 00 00 03 00 D0 00 00 0B FE 00
```

```
C0   13 10 00 00 00 F0 FF 00 00 00 00 00 00 00 00 00
D0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0   03 00 00 07 00 00 00 00 00 00 00 00 00 00 00 00
F0   03 30 00 D0 00 00 00 00 00 00 00 00 00 00 00 00
```

Description Host Bridge Location bus 0 (0x00), device 24 (0x18), function 2 (0x02) Common header

```
Vendor ID        0x1022
Model ID         0x1202
Revision ID      0x00
PI          0x00
SubClass         0x00
BaseClass        0x06
Cache Line       0x00
Latency          0x00
Header           0x80
```

PCI header

```
Subvendor ID        0x0000
Subsystem ID        0x0000
Int. Line        0x00
Int. Pin         0x00
```

PCI registers

```
     00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00   22 10 02 12 00 00 00 00 00 00 00 06 00 00 80 00
10   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
20   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
30   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
40   01 00 00 00 00 00 00 00 01 00 80 00 09 00 00 01
50   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
60   E0 3F 78 00 E0 3F 78 00 00 00 00 00 00 00 00 00
70   00 00 00 00 00 00 00 00 06 00 08 0E 50 04 02 18
80   77 00 00 00 D4 0A 24 00 45 96 16 00 66 B9 B2 01
90   00 00 01 00 0D 09 58 3F 07 03 00 80 00 00 00 00
A0   00 02 00 00 00 00 00 00 40 00 00 00 00 00 00 00
B0   8B AE A9 CB 99 00 00 00 38 B0 7E 00 FD CE EB 01
C0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0   DF 7E 91 C1 CF 07 BE 7F 7E D8 04 17 00 5E 9B EA
E0   F7 DF 9B 00 EC 87 DF 51 7F F0 6F 18 E4 03 6E 03
F0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

Description Host Bridge Location bus 0 (0x00), device 24 (0x18), function 3 (0x03) Common header

```
Vendor ID        0x1022
Model ID         0x1203
Revision ID      0x00
PI          0x00
```

```
SubClass          0x00
BaseClass         0x06
Cache Line        0x00
Latency           0x00
Header            0x80
```

PCI header

```
Subvendor ID         0x0000
Subsystem ID         0x0000
Int. Line        0x00
Int. Pin         0x00
```

PCI capability

```
Caps class        Secure Device
Caps offset       0xF0
```

PCI registers

```
     00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00   22 10 03 12 00 00 10 00 00 00 00 06 00 00 80 00
10   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
20   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
30   00 00 00 00 F0 00 00 00 00 00 00 00 00 00 00 00
40   FF FF FF 3F 5C 00 B0 4A 00 00 00 00 00 00 00 00
50   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
60   00 00 00 00 05 62 5F 34 00 00 00 30 52 80 01 00
70   53 11 04 00 11 11 08 00 14 0C 20 00 14 09 09 00
80   81 87 0B E6 E6 41 E6 01 08 00 00 00 00 40 58 40
90   00 00 00 00 3F 0E 00 00 B0 FF 37 00 00 00 00 00
A0   00 08 03 B0 EF 0F 0C 00 4C 06 04 00 00 00 00 00
B0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0   00 00 00 00 26 0F 81 C8 16 17 32 03 32 63 47 01
E0   00 00 00 00 30 13 00 1E 59 7F 07 02 00 00 00 00
F0   0F 00 10 00 00 00 00 00 00 00 00 00 43 0F 10 00
```

Description Host Bridge Location bus 0 (0x00), device 24 (0x18), function 4 (0x04) Common header

```
Vendor ID         0x1022
Model ID          0x1204
Revision ID       0x00
PI           0x00
SubClass          0x00
BaseClass         0x06
Cache Line        0x00
Latency           0x00
Header            0x80
```

PCI header

```
Subvendor ID        0x0000
Subsystem ID        0x0000
Int. Line       0x00
Int. Pin        0x00
```

PCI registers

```
     00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00   22 10 04 12 00 00 00 00 00 00 00 06 00 00 80 00
10   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
20   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
30   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
40   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
60   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
80   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
A0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

Description VGA Controller Location bus 2 (0x02), device 0 (0x00), function 0 (0x00) Common header

```
Vendor ID       0x10DE
Model ID        0x0DE1
Revision ID     0xA1
PI          0x00
SubClass        0x00
BaseClass       0x03
Cache Line      0x10
Latency         0x00
Header          0x80
```

PCI header

```
Address 0 (memory)   0xDE000000
Address 1 (memory)   0xD0000000
Address 3 (memory)   0xDA000000
Address 5 (port)     0x0000EC00
Subvendor ID         0x19DA
Subsystem ID         0x1167
Int. Line       0x13
Int. Pin        0x01
```

PCI capability

```
Caps class        Power Management
Caps offset       0x60
Caps version          1.2
```

PCI capability

```
Caps class        Message Signalled Interrupts
Caps offset       0x68
```

PCI capability

```
Caps class        PCI Express
Caps offset       0x78
Device type       PCI-E Endpoint Device
Port              0
Version           2.0
Link width        16x (max 16x)
```

PCI capability

```
Caps class        Vendor Dependant
Caps offset       0xB4
```

Extended capabilities

```
Caps class        Virtual Channel
Caps offset       0x100
Caps class        Power Budgeting
Caps offset       0x128
Caps class        Vendor Specific
Caps offset       0x600
Vendor ID         1
Size              36
```

PCI registers

```
    00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00  DE 10 E1 0D 07 00 10 00 A1 00 00 03 10 00 80 00
10  00 00 00 DE 0C 00 00 D0 00 00 00 00 0C 00 00 DA
20  00 00 00 00 01 EC 00 00 00 00 00 00 DA 19 67 11
30  00 00 00 00 60 00 00 00 00 00 00 00 13 01 00 00
40  DA 19 67 11 00 00 00 00 00 00 00 00 00 00 00 00
50  01 00 00 00 01 00 00 00 CE D6 23 00 00 00 00 00
60  01 68 03 00 08 00 00 00 05 78 80 00 00 00 00 00
70  00 00 00 00 00 00 00 00 10 B4 02 00 E0 8D 00 00
80  10 29 00 00 01 4D 05 00 08 01 01 11 00 00 00 00
90  00 00 00 00 00 00 00 00 00 00 00 00 10 00 00 00
A0  00 00 00 00 00 00 00 00 01 00 00 00 00 00 00 00
```

```
B0   00 00 00 00 09 00 14 01 00 00 00 00 00 00 00 00
C0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
100  02 00 81 12 00 00 00 00 00 00 00 00 00 00 00 00
110  00 00 00 00 FF 00 00 80 00 00 00 00 00 00 00 00
120  00 00 00 00 00 00 00 00 04 00 01 60 00 00 00 00
130  21 81 07 00 00 00 00 00 00 00 00 20 00 00 FF 63
```

Description Multimedia device Location bus 2 (0x02), device 0 (0x00), function 1 (0x01) Common header

```
Vendor ID        0x10DE
Model ID         0x0BEA
Revision ID      0xA1
PI          0x00
SubClass         0x03
BaseClass        0x04
Cache Line       0x10
Latency          0x00
Header           0x80
```

PCI header

```
Address 0 (memory)  0xDFE7C000
Subvendor ID        0x19DA
Subsystem ID        0x1167
Int. Line       0x12
Int. Pin        0x02
```

PCI capability

```
Caps class      Power Management
Caps offset     0x60
Caps version       1.2
```

PCI capability

```
Caps class      Message Signalled Interrupts
Caps offset     0x68
```

PCI capability

```
Caps class      PCI Express
Caps offset     0x78
Device type     PCI-E Endpoint Device
Port            0
Version         2.0
Link width      16x (max 16x)
```

PCI registers

```
     00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00   DE 10 EA 0B 06 01 10 00 A1 00 03 04 10 00 80 00
10   00 C0 E7 DF 00 00 00 00 00 00 00 00 00 00 00 00
20   00 00 00 00 00 00 00 00 00 00 00 00 DA 19 67 11
30   00 00 00 00 60 00 00 00 00 00 00 00 12 02 00 00
40   DA 19 67 11 00 00 00 00 00 00 00 00 00 00 00 00
50   00 00 00 00 00 00 00 00 CE D6 23 00 00 00 00 00
60   01 68 03 00 08 00 00 00 05 78 80 00 00 00 00 00
70   00 00 00 00 00 00 00 00 10 00 02 00 A0 8D 00 00
80   10 28 00 00 01 4D 05 00 0B 01 01 11 00 00 00 00
90   00 00 00 00 00 00 00 00 00 00 00 00 10 00 00 00
A0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
B0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
C0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
D0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
E0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
F0   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
100  00 00 00 00 00 00 00 00 00 00 00 00 11 20 06 00
110  00 20 00 00 00 20 00 00 00 00 00 00 00 00 00 00
120  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
130  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

Description USB Controller Location bus 3 (0x03), device 0 (0x00), function 0 (0x00) Common header

```
Vendor ID        0x1912
Model ID         0x0014
Revision ID      0x03
PI          0x30
SubClass         0x03
BaseClass        0x0C
Cache Line       0x10
Latency          0x00
Header           0x00
```

PCI header

```
Address 0 (memory)  0xDFFFE000
Subvendor ID        0x0000
Subsystem ID        0x0000
Int. Line      0x05
Int. Pin       0x01
```

PCI capability

```
Caps class      Power Management
Caps offset     0x50
Caps version       1.2
```

PCI capability

```
Caps class        Message Signalled Interrupts
Caps offset       0x70
```

PCI capability

```
Caps class        MSI-X
Caps offset       0x90
```

PCI capability

```
Caps class        PCI Express
Caps offset       0xA0
Device type       PCI-E Endpoint Device
Port              0
Version           2.0
Link width        1x (max 1x)
```

Extended capabilities

```
Caps class        Advanced Error Reporting
Caps offset       0x100
Caps class        0x18
Caps offset       0x150
```

PCI registers

```
     00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
00   12 19 14 00 06 01 10 00 03 30 03 0C 10 00 00 00
10   04 E0 FF DF 00 00 00 00 00 00 00 00 00 00 00 00
20   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
30   00 00 00 00 50 00 00 00 00 00 00 00 05 01 00 00
40   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50   01 70 C3 C9 08 00 00 00 00 00 00 00 00 00 00 00
60   30 20 00 00 00 00 00 00 00 00 00 00 04 06 20 00
70   05 90 86 00 00 00 00 00 00 00 00 00 00 00 00 00
80   00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
90   11 A0 07 00 00 10 00 00 80 10 00 00 00 00 00 00
A0   10 00 02 00 C0 8F 00 00 10 28 19 00 12 EC 07 00
B0   00 00 11 10 00 00 00 00 00 00 00 00 00 00 00 00
C0   00 00 00 00 10 08 00 00 00 00 00 00 00 00 00 00
D0   02 00 01 00 00 00 00 00 00 00 00 00 00 00 00 00
E0   00 00 00 00 00 00 55 00 00 00 01 05 7F 20 9D 01
F0   00 00 00 00 00 00 00 80 00 00 00 00 00 00 00 00
100  01 00 01 15 00 00 00 00 00 00 00 00 30 20 06 00
110  00 20 00 00 00 20 00 00 00 00 00 00 00 00 00 00
120  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
130  00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

DMI BIOS

```
vendor          American Megatrends Inc.
version         0403
date            03/08/2010
ROM size        1024 KB
```

DMI System Information

```
manufacturer       System manufacturer
product        System Product Name
version        System Version
serial         System Serial Number
UUID           {B517E080-FE8D-11D5-89C0-E0CB4ECAFC74}
SKU         To Be Filled By O.E.M.
family         To Be Filled By O.E.M.
```

DMI Baseboard

```
vendor          ASUSTeK Computer INC.
model           M4N68T Pro
revision        Rev X.0x
serial          MT7003013600589
```

DMI System Enclosure

```
manufacturer       Chassis Manufacture
chassis type       Desktop
chassis serial     Chassis Serial Number
```

DMI Processor

```
manufacturer       AMD
model           AMD Phenom(tm) II X4 945 Processor
clock speed     3000.0 MHz
FSB speed       200.0 MHz
multiplier      15.0x
```

DMI Memory Controller

```
correction      64-bit ECC
Max module size    2048 MBytes
```

DMI Memory Module

```
designation        DIMM0
```

```
size              2048 MBytes (single bank)
```

DMI Memory Module

```
designation       DIMM1
size              2048 MBytes (single bank)
```

DMI Memory Module

```
designation       DIMM2
size              4096 MBytes (double bank)
```

DMI Memory Module

```
designation       DIMM3
```

DMI Port Connector

```
designation       PS/2 KeyBoard (internal)
designation       Keyboard (external)
port type         Keyboard Port
connector         PS/2
```

DMI Port Connector

```
designation       PS/2 Mouse (internal)
designation       PS/2 Mouse (external)
port type         Mouse Port
connector         PS/2
```

DMI Port Connector

```
designation       USB12 (internal)
designation       USB12 (external)
port type         USB
connector         Access Bus (USB)
```

DMI Port Connector

```
designation       USB34 (internal)
designation       USB34 (external)
port type         USB
connector         Access Bus (USB)
```

DMI Port Connector

```
designation       USB56 (internal)
designation       USB56 (external)
port type         USB
```

```
connector        Access Bus (USB)
```

DMI Port Connector

```
designation     USB78 (internal)
designation     USB78 (external)
port type       USB
connector       Access Bus (USB)
```

DMI Port Connector

```
designation     USB910 (internal)
designation     USB910 (external)
port type       USB
connector       Access Bus (USB)
```

DMI Port Connector

```
designation     LPT Port (internal)
designation     LPT 1 (external)
port type       Parallel Port ECP/EPP
connector       DB-25 male
```

DMI Port Connector

```
designation     COM Port (internal)
designation     COM A (external)
port type       Serial Port 16550A
connector       DB-9 male
```

DMI Port Connector

```
designation     Audio_Line_In (internal)
designation     Audio_Line_In (external)
port type       Audio Port
connector       Mini Jack (headphones)
```

DMI Port Connector

```
designation     Audio_Line_Out (internal)
designation     Audio_Line_Out (external)
port type       Audio Port
connector       Mini Jack (headphones)
```

DMI Port Connector

```
designation     Audio_Mic_In (internal)
designation     Audio_Mic_In (external)
port type       Audio Port
connector       Mini Jack (headphones)
```

DMI Port Connector

designation	LAN (internal)
designation	LAN (external)
port type	Network Port
connector	RJ-45

DMI Port Connector

designation	SPDIF_OUT (internal)

DMI Port Connector

designation	PRI IDE (internal)
connector	On Board IDE

DMI Port Connector

designation	AAFP (internal)

DMI Port Connector

designation	SPEAKER (internal)

DMI Port Connector

designation	PANEL (internal)
connector	9 Pin Dual Inline (pin 10 cut)

DMI Port Connector

designation	CPU FAN (internal)

DMI Port Connector

designation	CHA FAN (internal)

DMI Port Connector

designation	SATA1 (internal)

DMI Port Connector

designation	SATA2 (internal)

DMI Port Connector

designation	SATA3 (internal)

DMI Port Connector

designation	SATA4 (internal)

DMI Extension Slot

designation	PCIEX16
type	A5
populated	yes

DMI Extension Slot

designation	PCIEX1_1
type	A5
populated	yes

DMI Extension Slot

designation	PCIEX1_2
type	A5
populated	no

DMI Extension Slot

designation	PCI1
type	PCI
width	32 bits
populated	no

DMI Extension Slot

designation	PCI2
type	PCI
width	32 bits
populated	no

DMI Extension Slot

designation	PCI3
type	PCI
width	32 bits
populated	no

DMI Extension Slot

designation	PCI4
type	PCI
width	32 bits
populated	no

DMI OEM Strings

```
string[0]       E0CB4ECAFC74
string[1]       To Be Filled By O.E.M.
string[2]       To Be Filled By O.E.M.
string[3]       To Be Filled By O.E.M.
```

DMI Physical Memory Array

```
location        Motherboard
usage           System Memory
correction      None
max capacity       16384 MBytes
max# of devices    4
```

DMI Memory Device

```
designation     DIMM0
format          DIMM
type            unknown
total width     64 bits
data width      64 bits
size            2048 MBytes
```

DMI Memory Device

```
designation     DIMM1
format          DIMM
type            unknown
total width     64 bits
data width      64 bits
size            2048 MBytes
```

DMI Memory Device

```
designation     DIMM2
format          DIMM
type            unknown
total width     64 bits
data width      64 bits
size            4096 MBytes
```

DMI Memory Device

```
designation     DIMM3
format          DIMM
type            unknown
```

Storage

Drive 0

```
Device Path
\\?\scsi#disk&ven_maxtor_6&prod_l250s0#4&2cf640f2&0&000000#{53f56307-
b6bf-11d0-94f2-00a0c91efb8b}
Type            Fixed
Name            Maxtor 6 L250S0
Capacity        232.9 GB
SMART Support       Yes
Volume          c:\, 232.8 GBytes (47.2 percent available)
```

USB Devices

USB Device USB-Eingabegerät, class=0x00, subclass=0x00, vendor=0x046D, product=0xC019 USB Device Arduino Mega 2560, class=0x02, subclass=0x00, vendor=0x2341, product=0x0042 USB Device USB-Eingabegerät, class=0x00, subclass=0x00, vendor=0x413C, product=0x2003

Graphics

Number of adapters 1

Graphic APIs

API NVIDIA I/O API NVIDIA NVAPI

Display Adapters

Display adapter 0

```
Name            NVIDIA GeForce GT 430
Board Manufacturer  ZOTAC International Ltd.
Revision        A1
Memory size     1024 MB
Memory type     DDR3
PCI device      bus 2 (0x2), device 0 (0x0), function 0 (0x0)
Vendor ID       0x10DE (0x19DA)
Model ID        0x0DE1 (0x1167)
Performance Level   Default
    Core clock    50.5 MHz
    Shader clock    101.0 MHz
    Memory clock    135.0 MHz
```

```
Performance Level    2D Desktop
    Core clock     405.0 MHz
    Shader clock    810.0 MHz
    Memory clock    324.0 MHz
Performance Level    3D Applications
    Core clock     700.0 MHz
    Shader clock   1400.0 MHz
    Memory clock    800.0 MHz
```

Win32_VideoController AdapterRAM = 0x40000000 (1073741824) Win32_VideoController DriverVersion = 10.18.13.5382 Win32_VideoController DriverDate = 08/07/2015

Monitor 0

```
Model            SyncMaster (Samsung)
ID          SAM02B6
Serial         HS1Q306190
Manufacturing Date  Week 11, Year 2008
Size         24.0 inches
Max Resolution     1920 x 1200 @ 59 Hz
Horizontal Freq. Range  30-81 kHz
Vertical Freq. Range   56-75 Hz
Max Pixel Clock    170 MHz
Gamma Factor      2.6
```

Software

Windows Version Microsoft Windows 7 (6.1) Professional 64-bit Service Pack 1 (Build 7601) DirectX Version 11.0

Register Spaces

Register space PCI Register space PCI #2 Register space PCI Express, base address = 0x0E0000000 Register space class = 0x12 Register space SMBus, base address = 0x0600 Register space SMBus, base address = 0x0700 Register space I2C Register space LPC, base address = 0x0290 Register space LPC, base address = 0x02E